

# PICO-NYMPH-GL REV:A1

L=6, 100 x 72 mm

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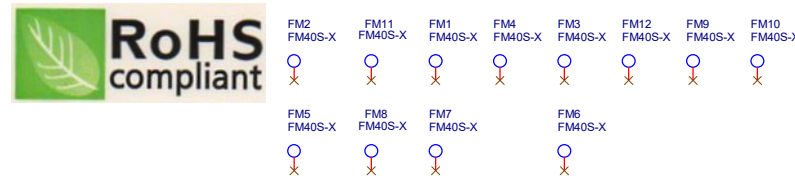
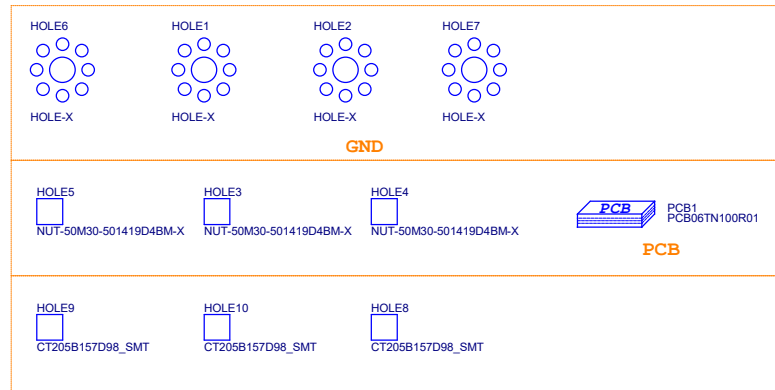
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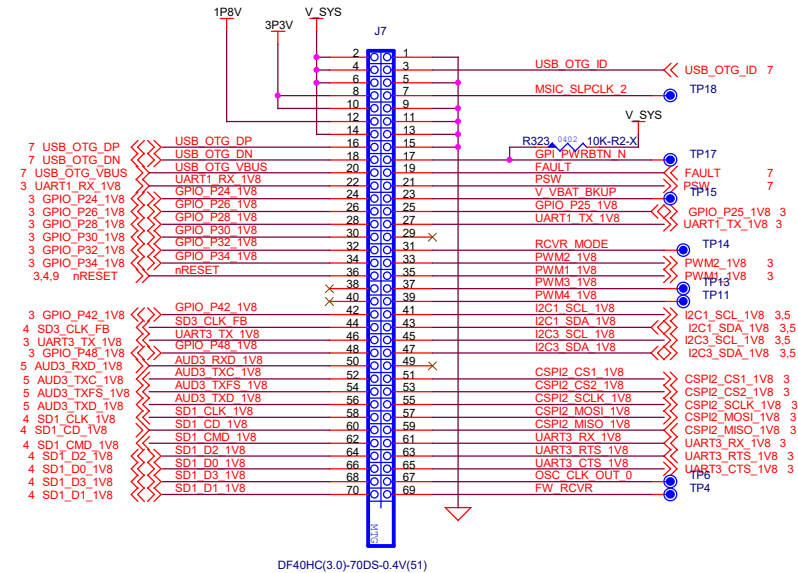
P11 REVISION HISTORY



Note : Components marked with -X means the components are not populated (DNP).

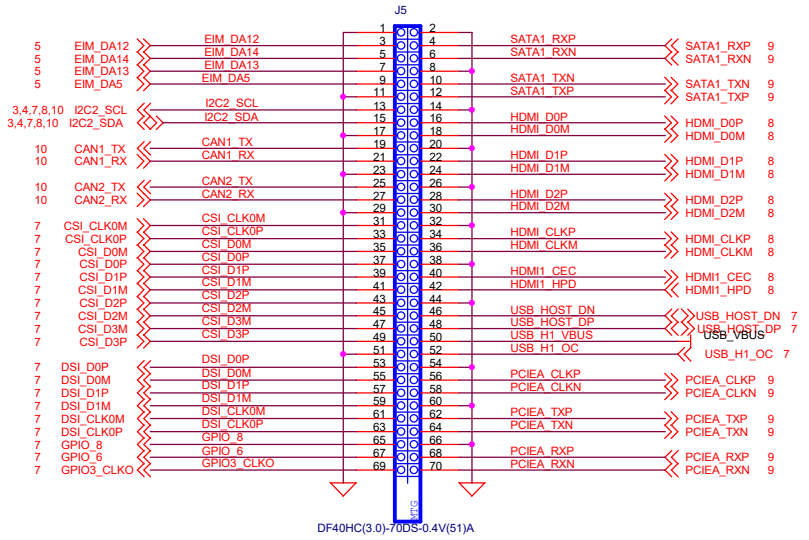
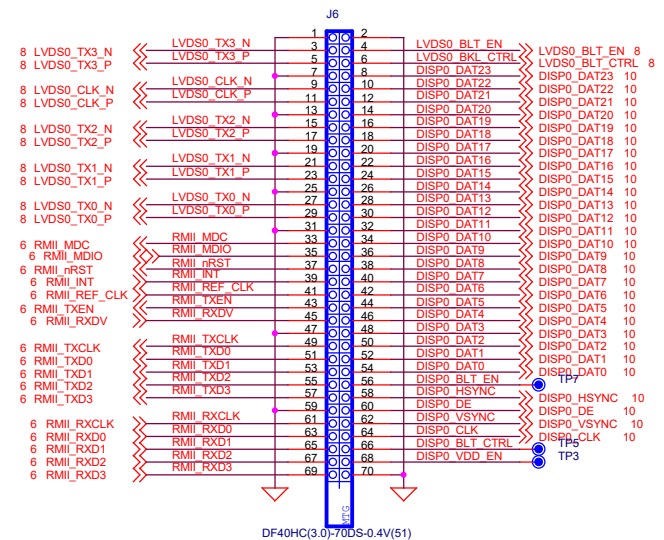
<b>TechNexion</b>		
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# BOARD TO BOARD CONNECTOR

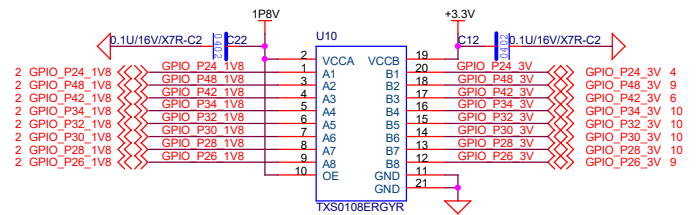
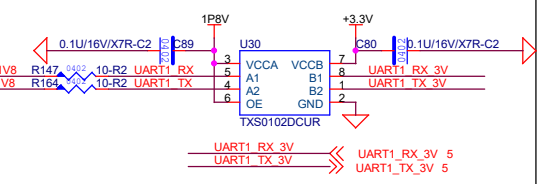
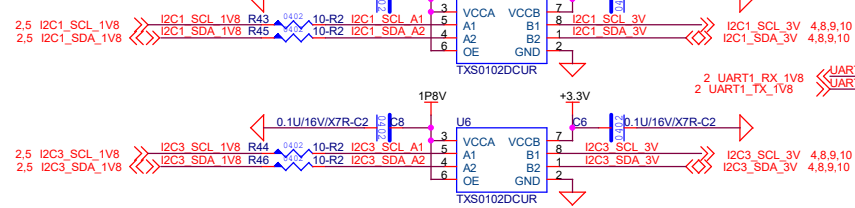
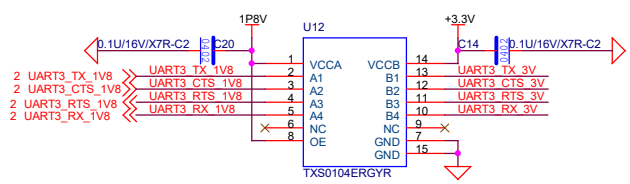


I2C Table I2C & Devices.

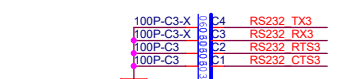
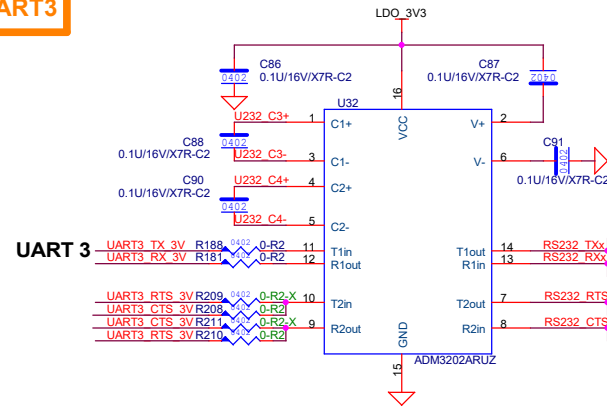
Devices	PICO NYMPH BUS NAME	PICO IMX6 EQUIVALENT	PICO IMX6UL EQUIVALENT	PICO IMX7 EQUIVALENT
ADC	I2C1	I2C1	I2C2	I2C1
Audio	I2C1	I2C1	I2C2	I2C1
LVDS	I2C1	I2C1	I2C2	I2C1
EEPROM	I2C1	I2C1	I2C2	I2C1
RTC	I2C1	I2C1	I2C2	I2C1
MIKRO BUS	I2C2	I2C2	I2C1	I2C3
MIPI	I2C2	I2C2	I2C1	I2C3
HDMI	I2C2	I2C2	I2C1	I2C3
VGA	I2C3	I2C3	I2C3	I2C2



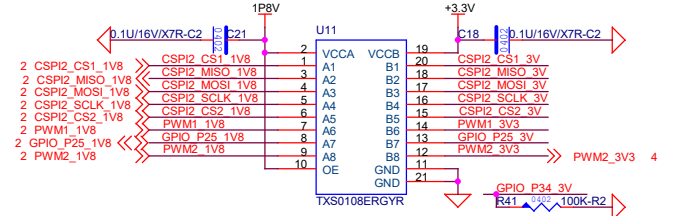
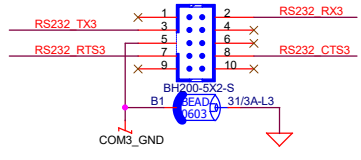
# I/O LEVEL SHIFT



# UART3

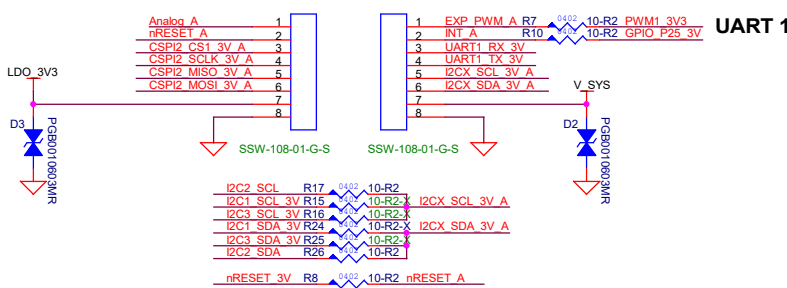


# COM1

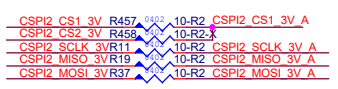


# MIKROBUS

## JP1 JP2

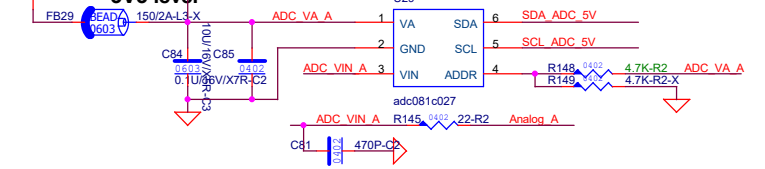


## UART 1

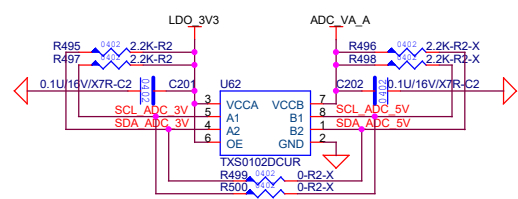
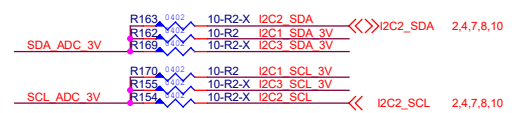
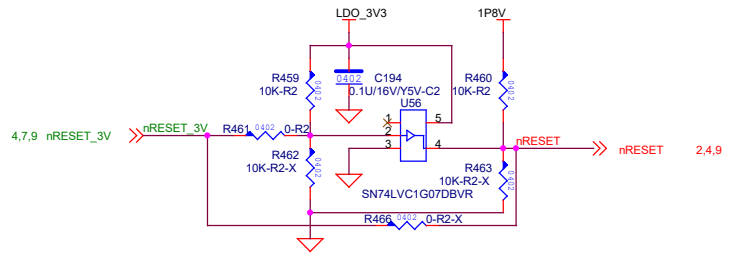
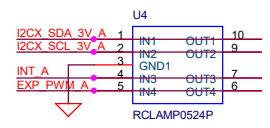
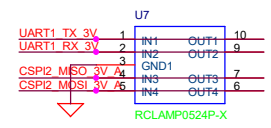
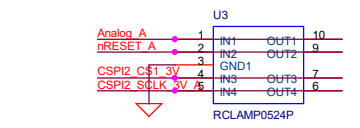


## 5V level

## 3V3 level



Address 0x52

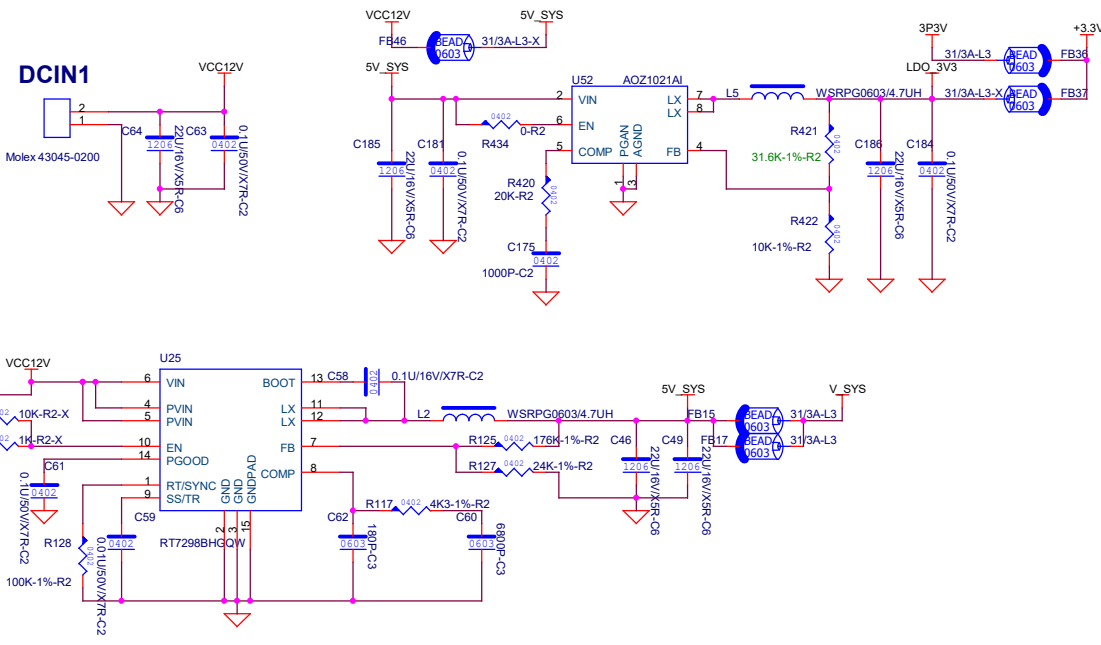


## TechNexion

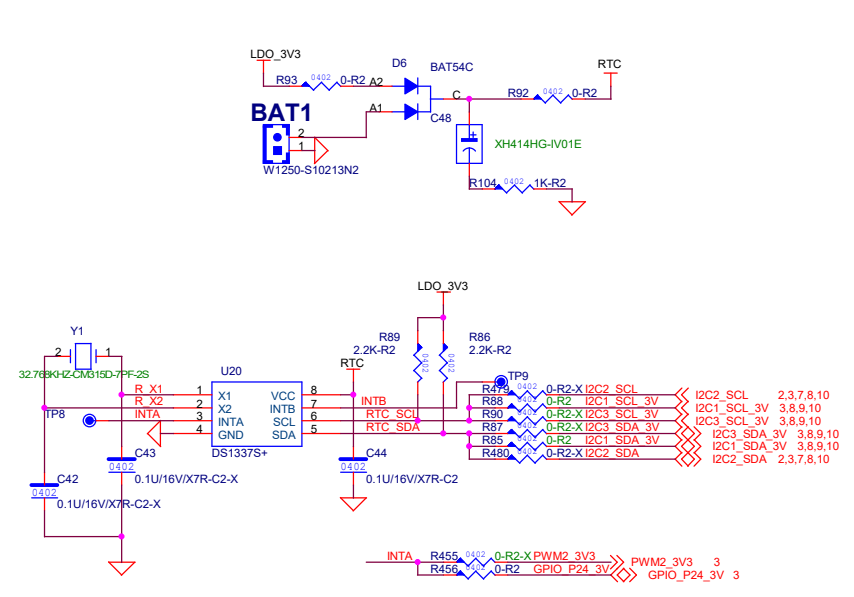
### MIKROBUS & UART HEADER

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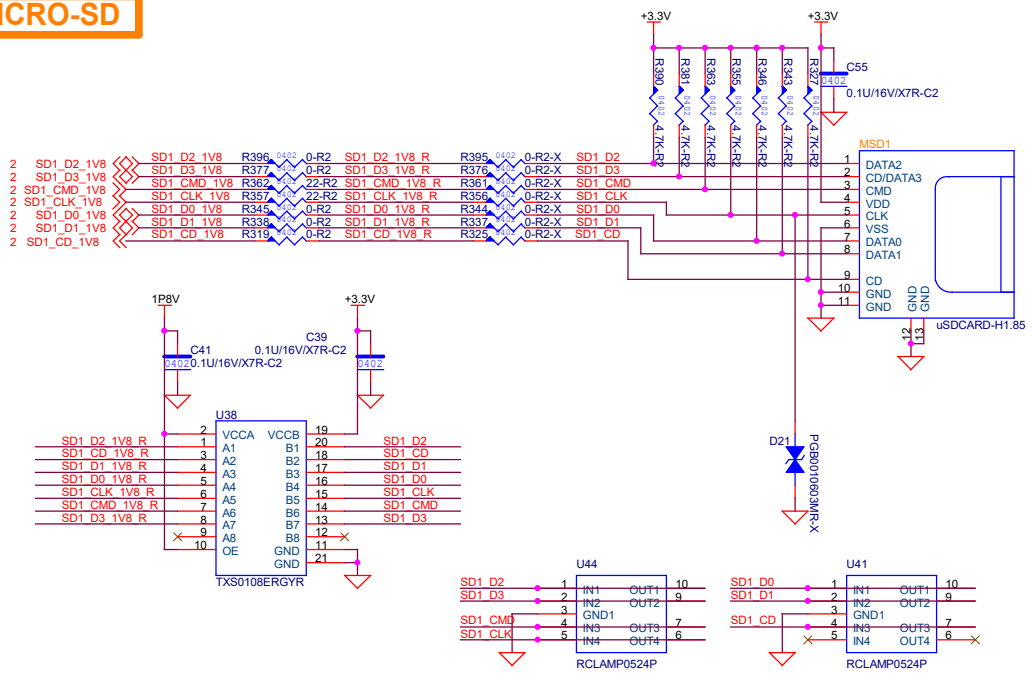
# DC-DC



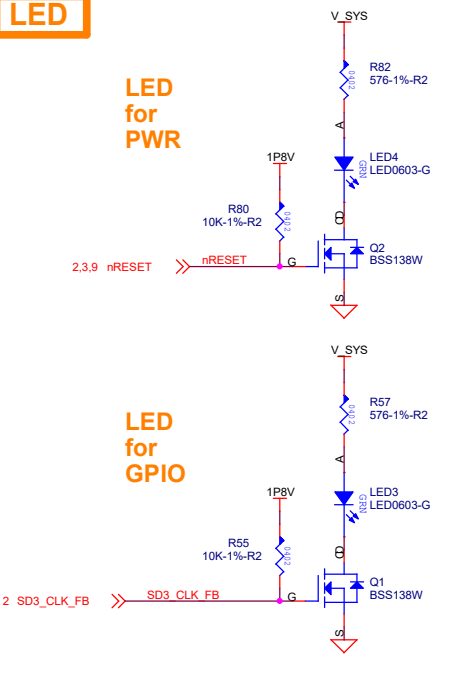
# RTC



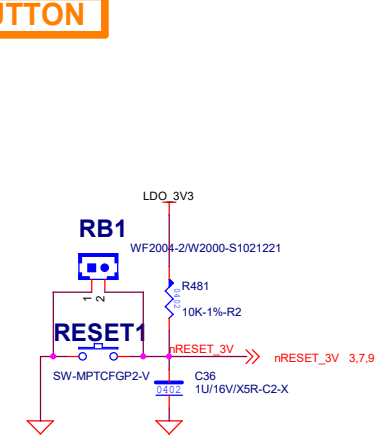
# MICRO-SD



# LED



# BUTTON

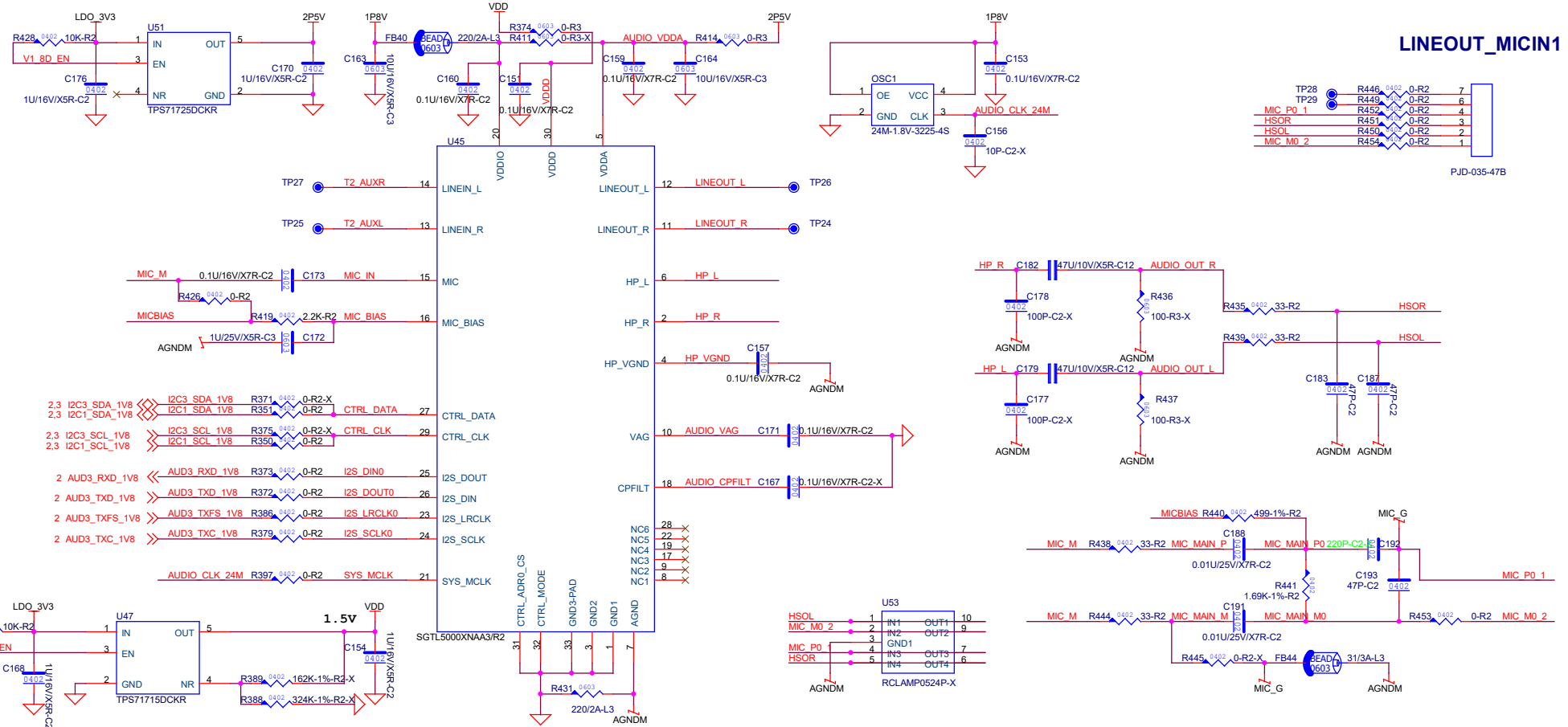


**TechNexion**

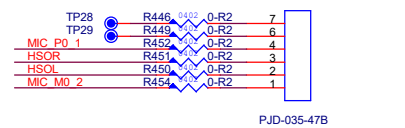
**DC-DC/SD/LED/RESET**

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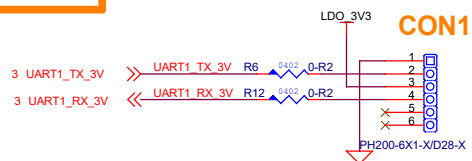
# AUDIO



# LINEOUT\_MICIN1



# CONSOLE



**CON1 Pin define.**

Pin	Signal
1	GND
2	TX
3	3.3V
4	RX
5	NC
6	NC

# BOOT PIN HEADER

## PICO IMX6/IMX6QPOP BOOT Device Jumper Settings

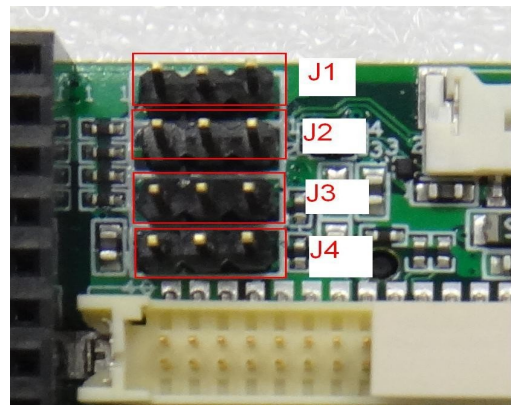
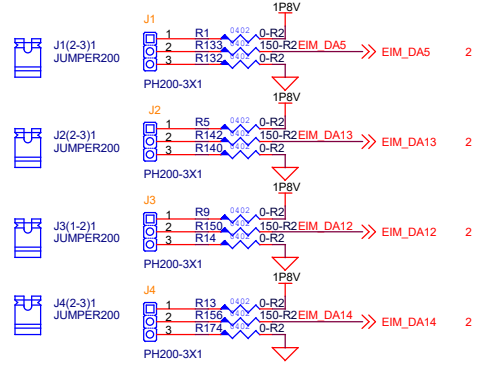
	J1	J2	J3	J4
SD3	2-3	1-2	1-2	2-3
SD1	2-3	1-2	2-3	2-3
eMMC	1-2	2-3	1-2	1-2

## PICO IMX6UL NAND BOOT Device Jumper Settings

	J1	J2	J3	J4
SD2	2-3	1-2	1-2	1-2
qSPI	1-2	2-3	2-3	1-2
NAND	2-3	2-3	1-2	2-3

## PICO IMX6UL eMMC BOOT Type Jumper Settings

MODE0	MODE1	BMODE [0:1]
J3	J4	
1-2	2-3	Serial Downloader
2-3	1-2	Internal Boot



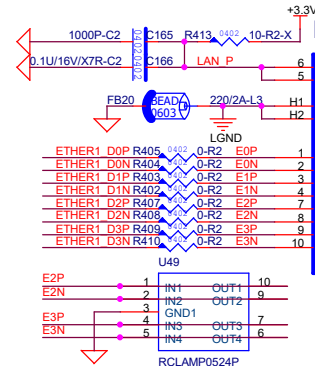
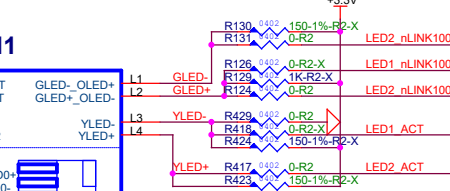
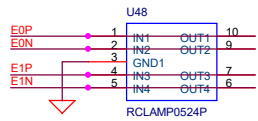
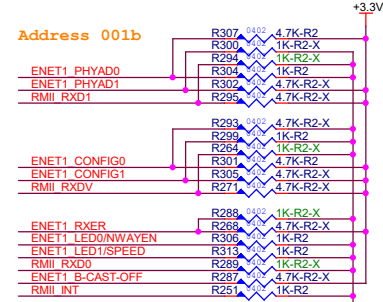
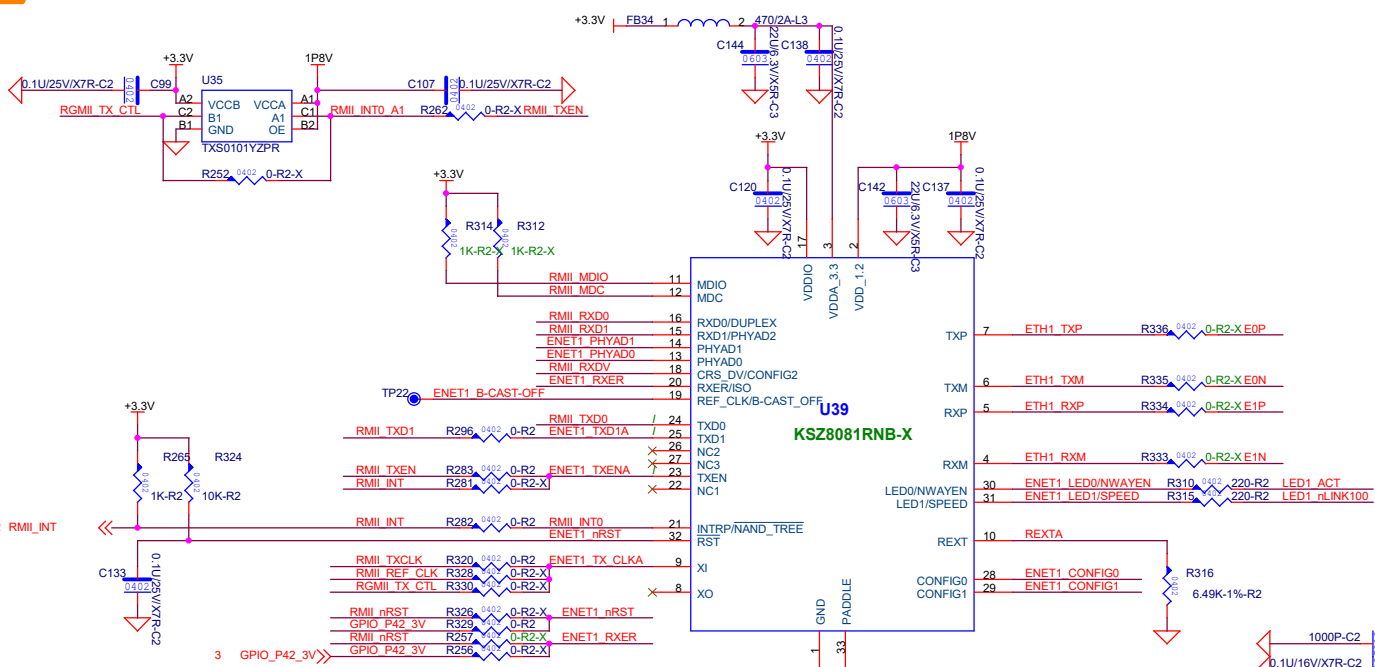
**TechNexion**

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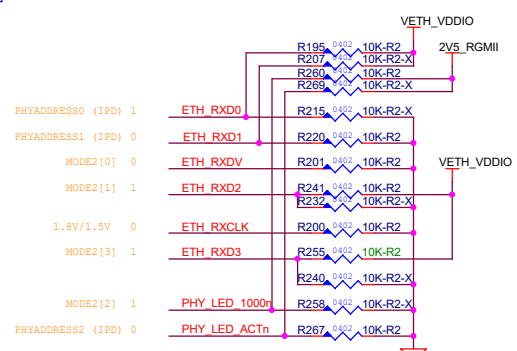
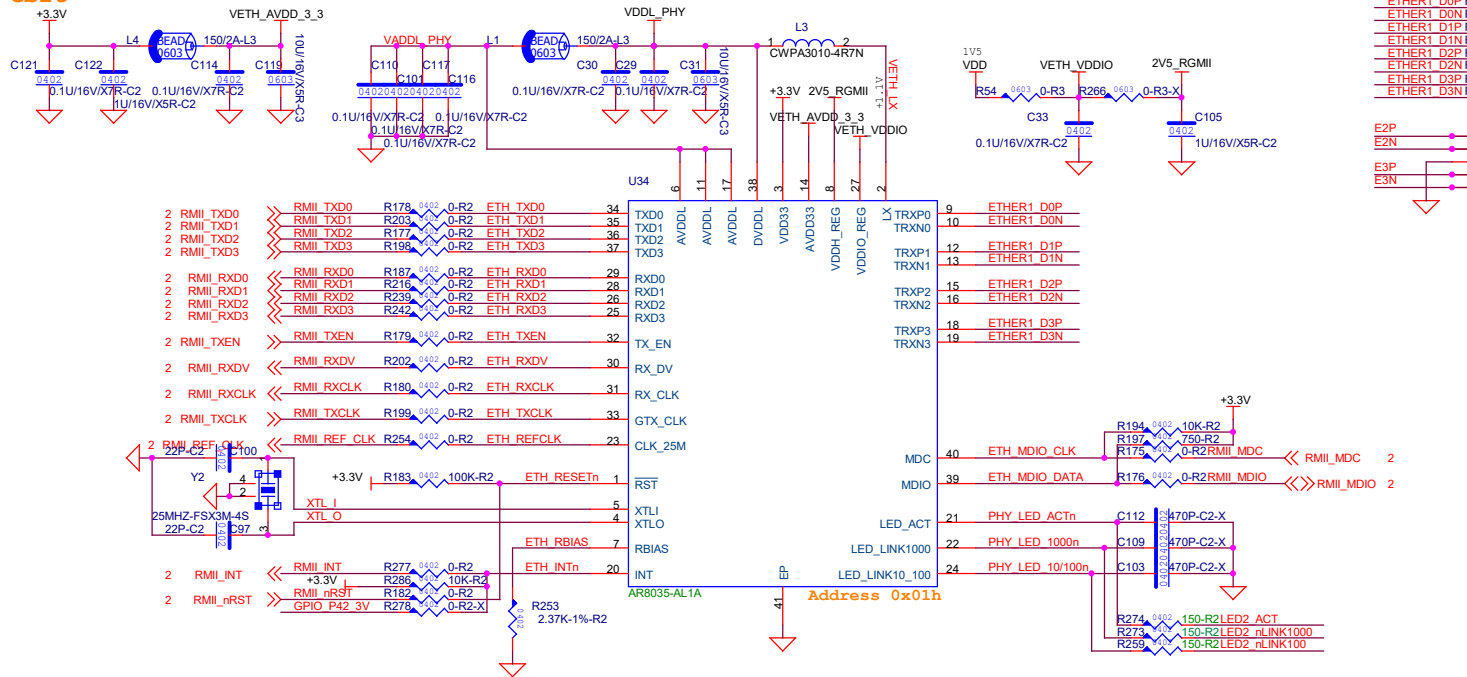
# RMII Option

100M



# RGMII

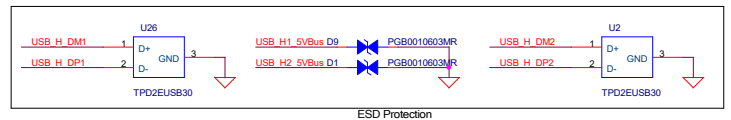
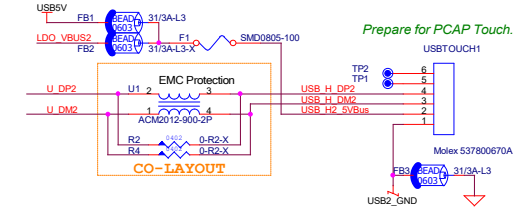
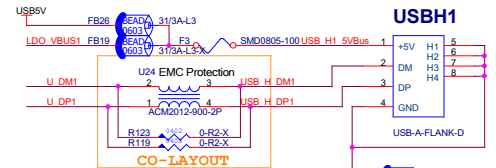
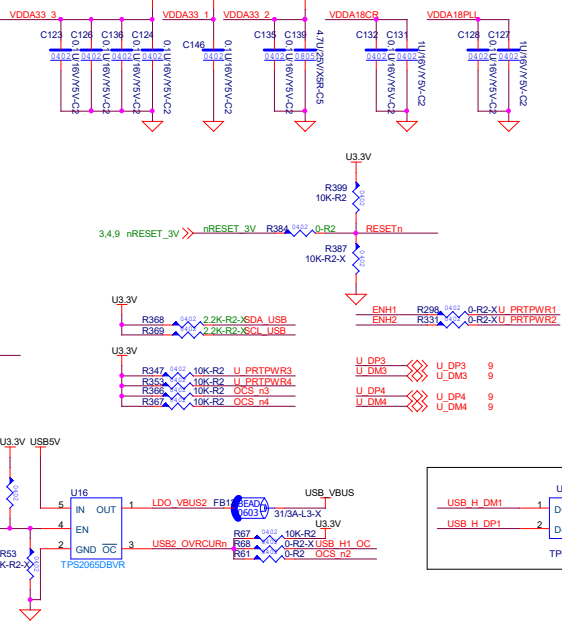
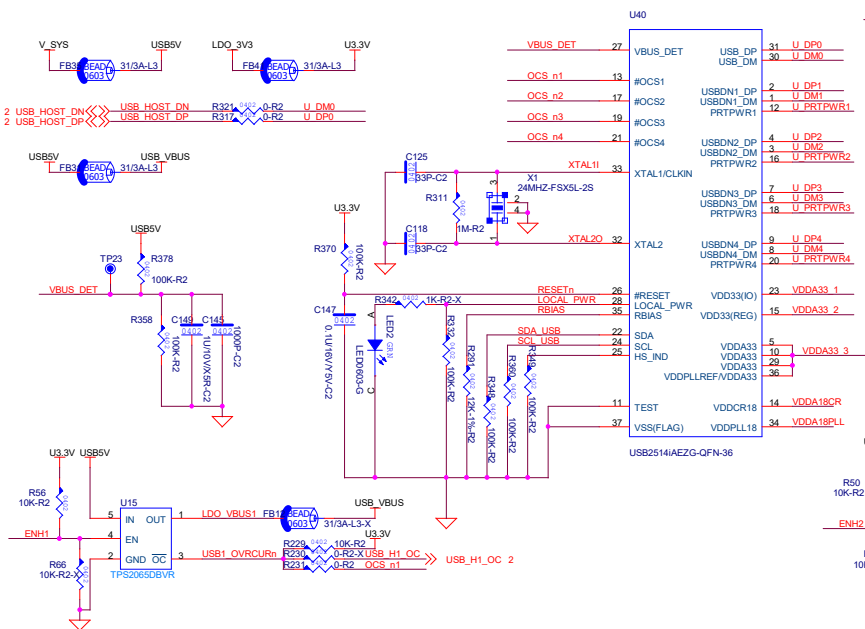
Gbit



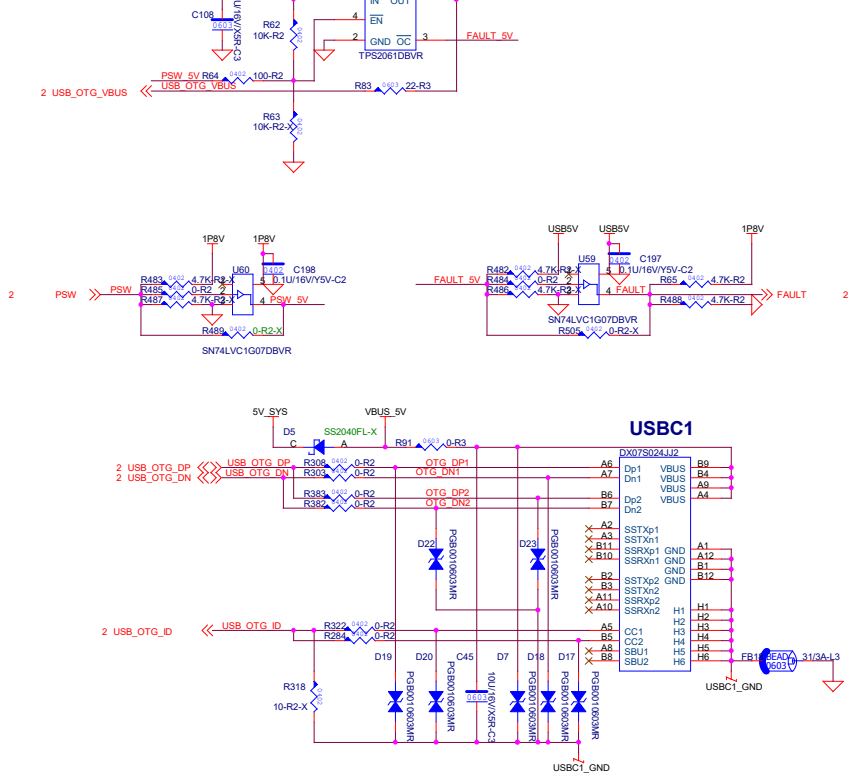
TechNexion  
ETHERNET

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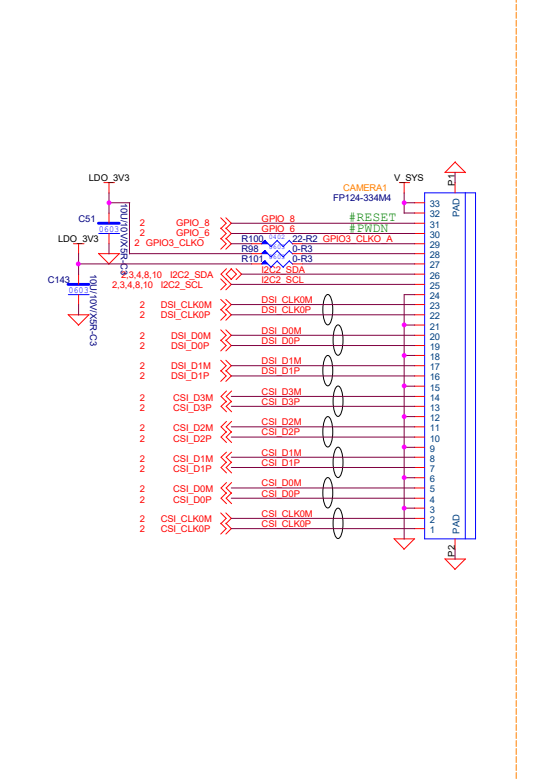
# USB HOST



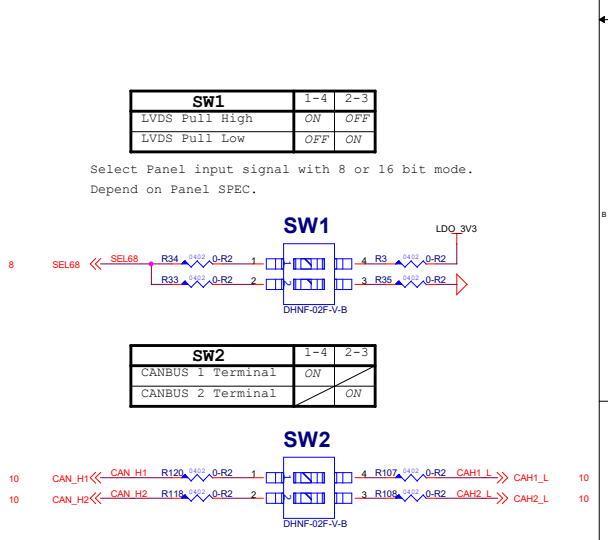
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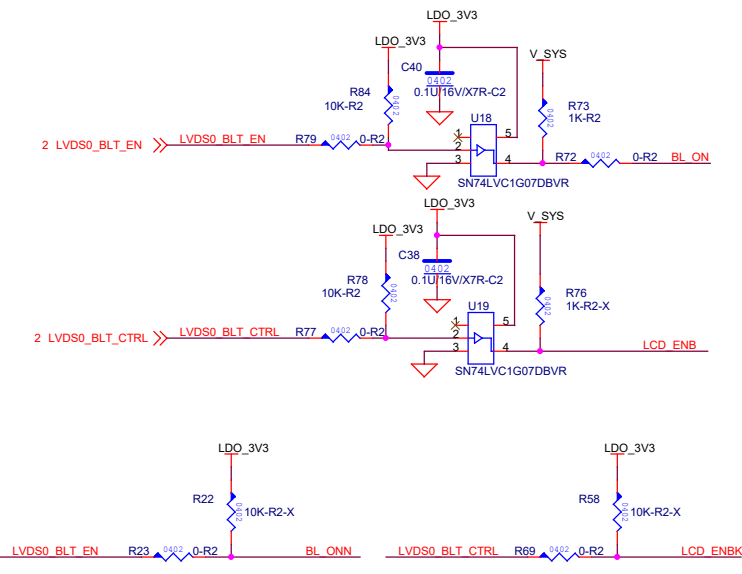
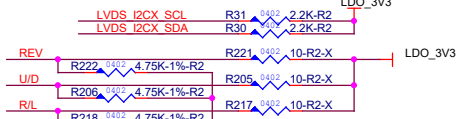
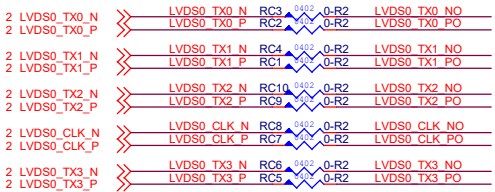
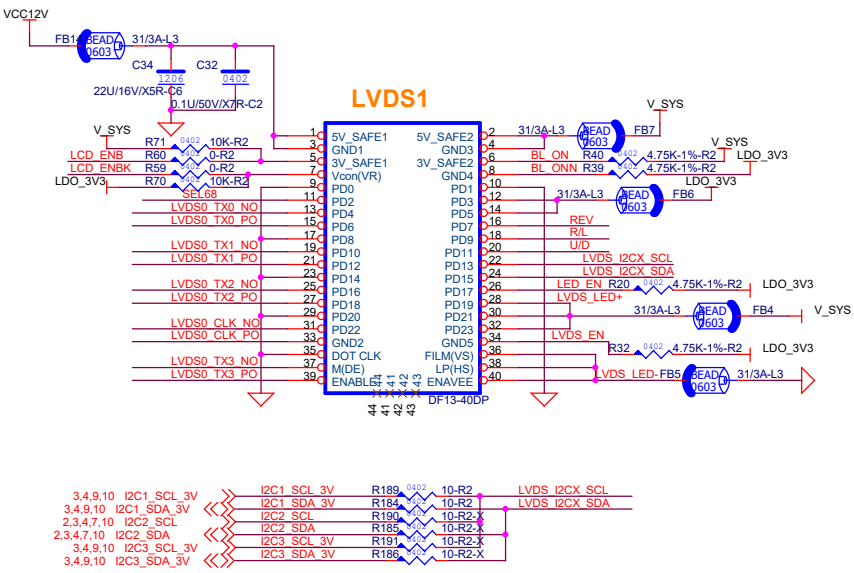
# CAMERA



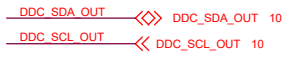
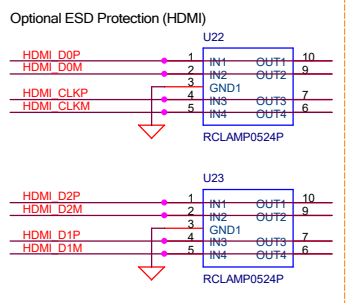
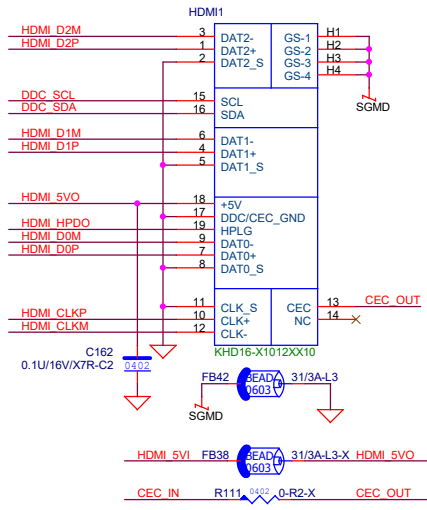
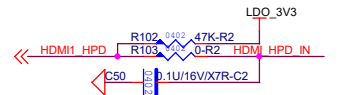
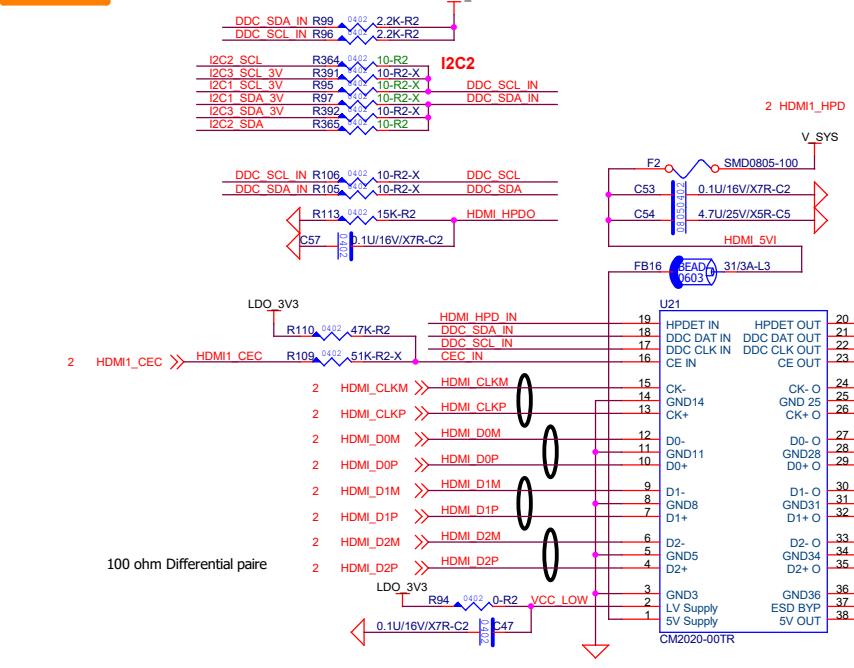
# LVDS SETTING



# LVDS



# HDMI

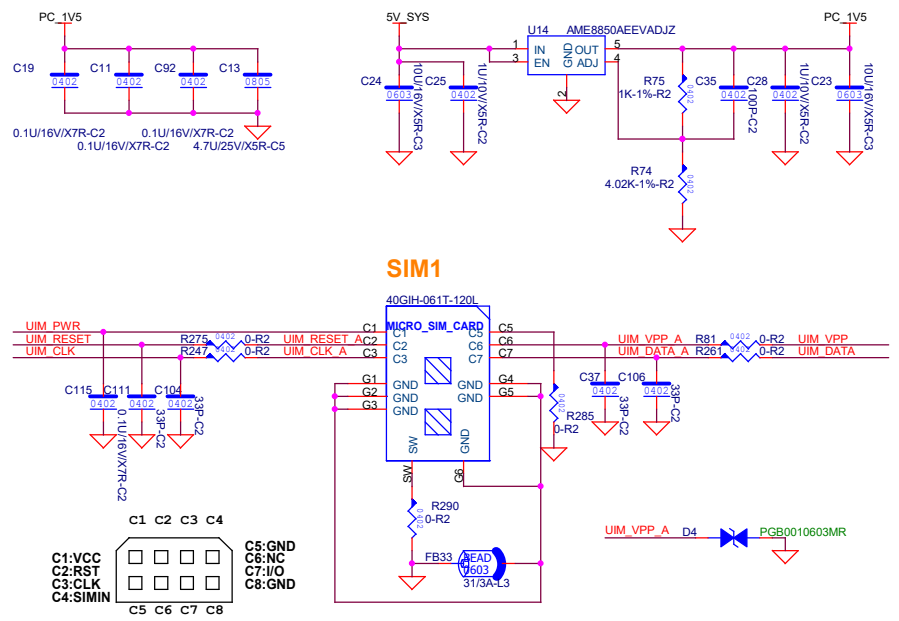
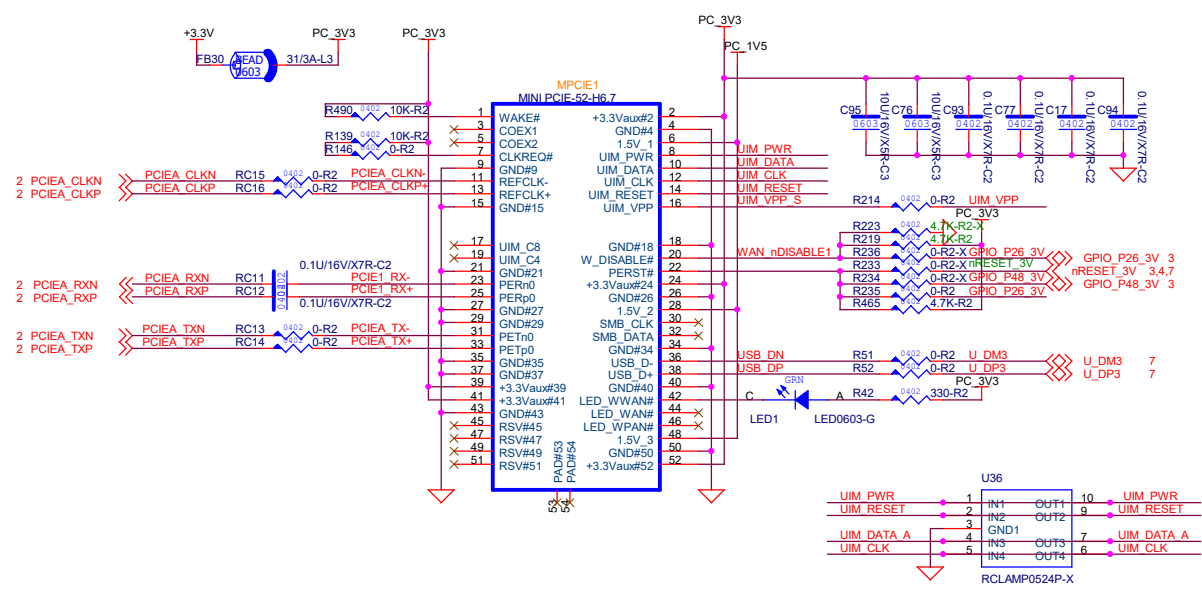


**TechNexion**  
LVDS & HDMI

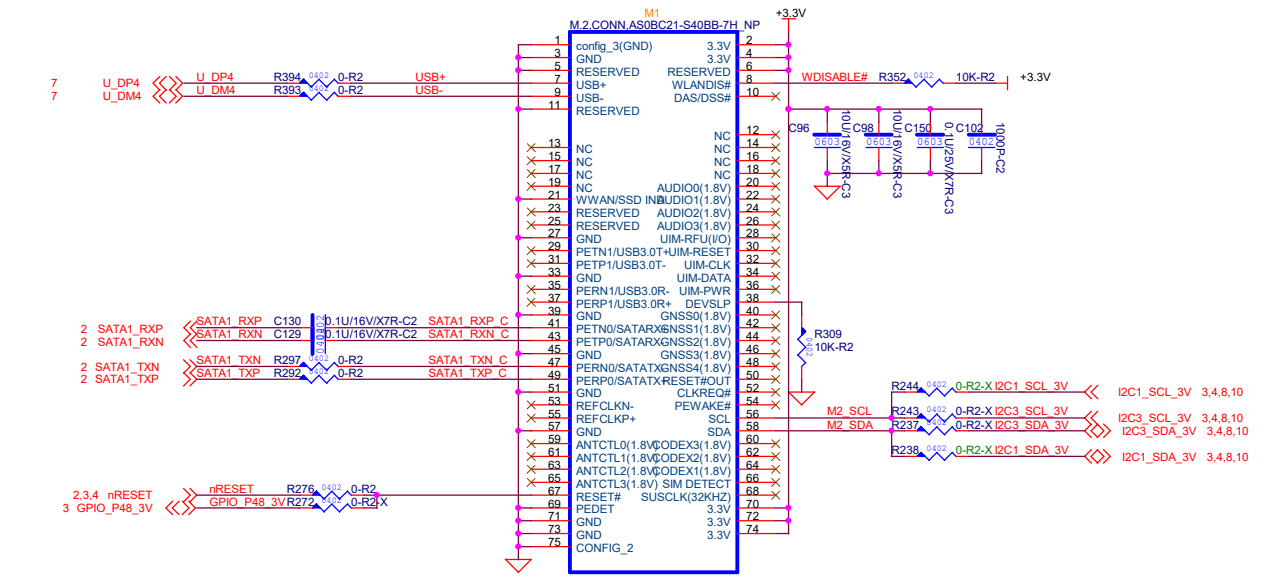
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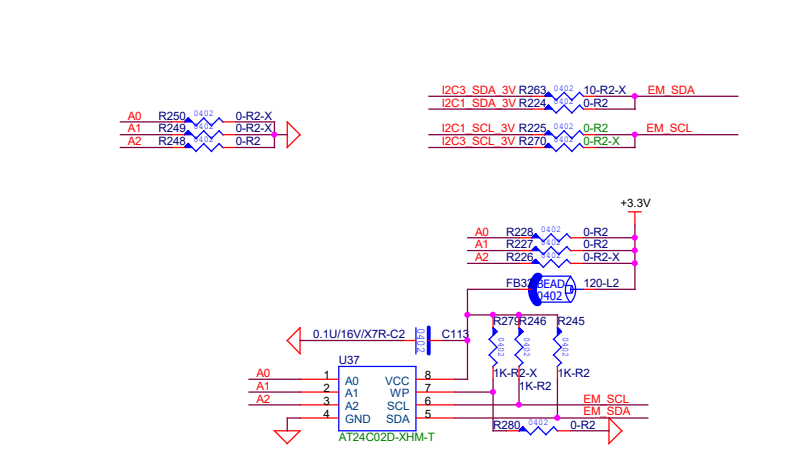
# MINI PCIE



# M.2 CONNECTOR

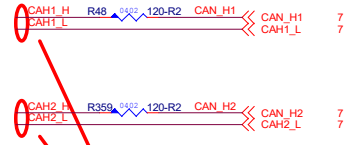
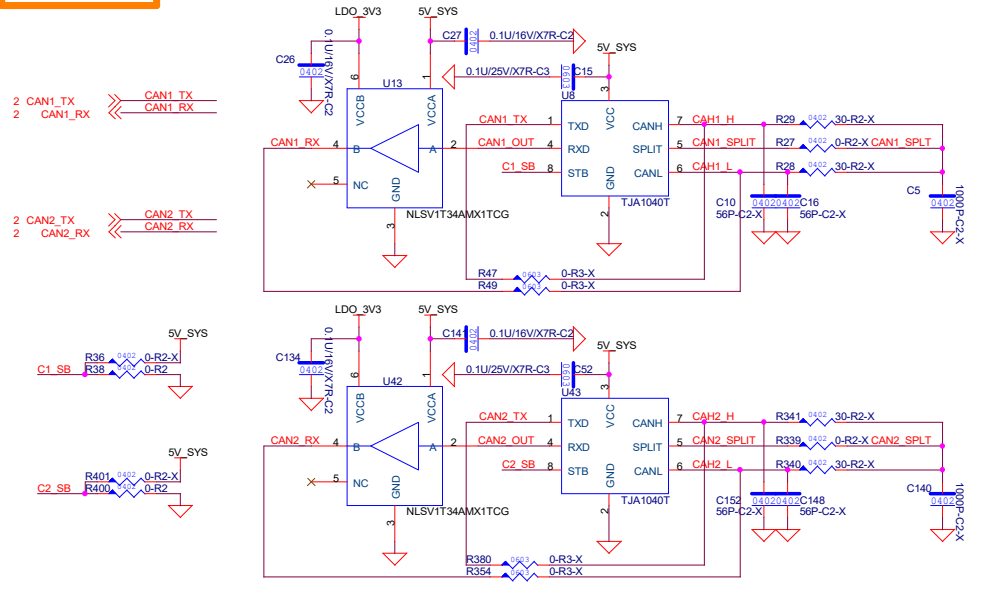


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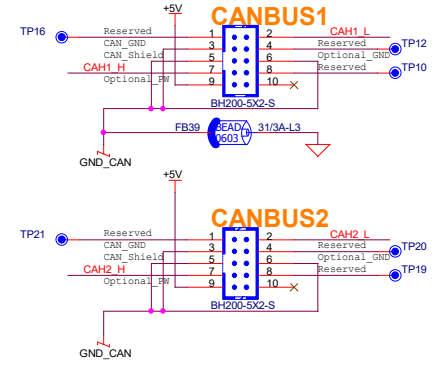


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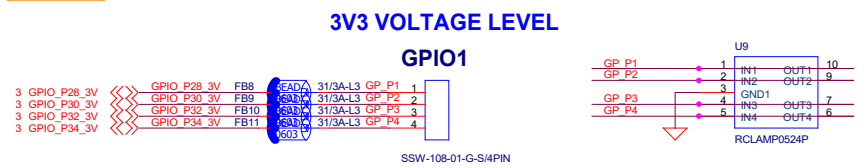
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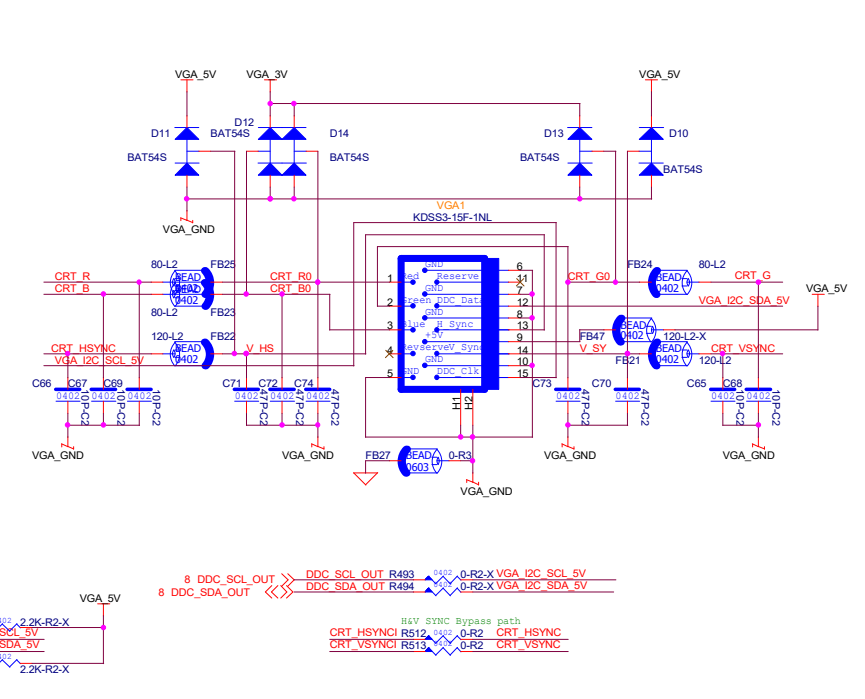
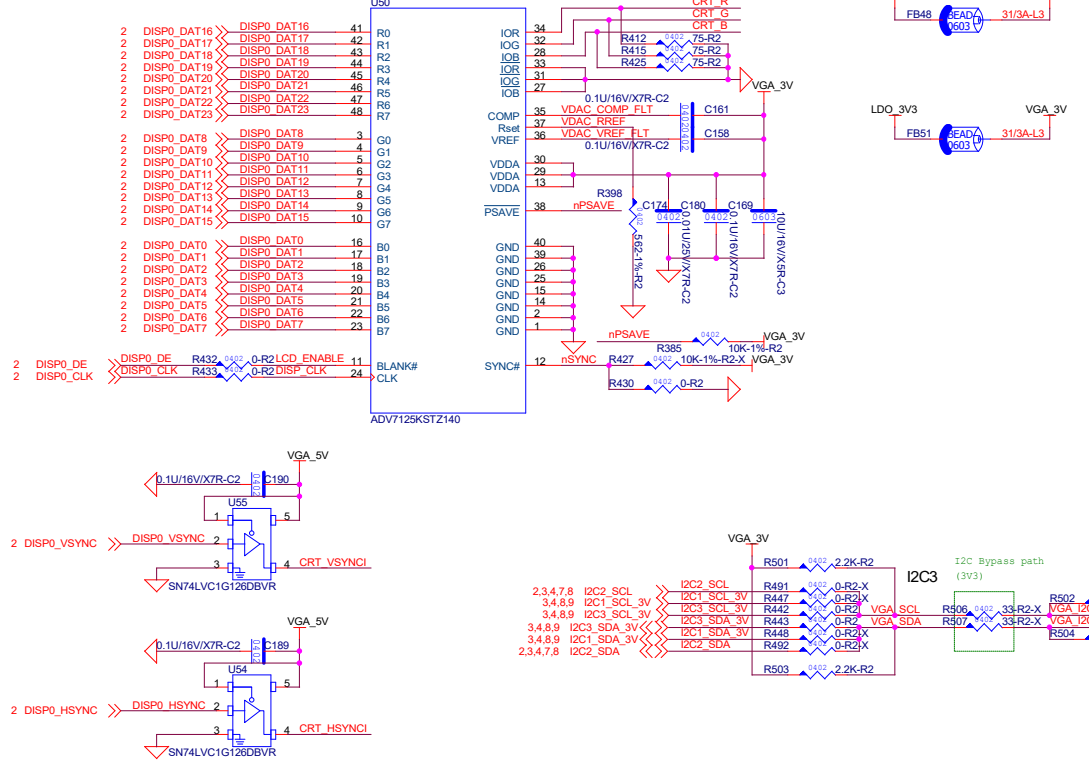
**LAYOUT NOTE:**  
 CAN Differential Pair, annotated with a ring around the pair. Follow differential routing guidelines.  
 Length Match: +/- 100 mils  
 Differential Impedance: 120 ohms  
 Minimize stub to RX on net CANp



# GPIO



# VGA



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<b>CAN/GPIO/VGA</b>		
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